



Serial No. 10/796,027

IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please AMEND claims 1 and 8 in accordance with the following:

1. (CURRENTLY AMENDED) A field-sequential liquid crystal display panel, comprising:
thin film transistors, each comprising a drain, a source, and a gate;
cell electrodes respectively coupled to the drains or sources of the thin film transistors;
scan electrode lines coupled to the gates of the thin film transistors;
data electrode lines coupled to the sources or drains of the thin film transistors; and
storage capacitors provided between each of the cell electrodes and a corresponding one of the scan electrode lines, to sustain voltages applied to the cell electrodes of the field-sequential liquid crystal display panel.
2. (CANCELLED)
3. (CANCELLED)
4. (ORIGINAL) The field-sequential liquid crystal display panel of claim 1, further comprising a data driver to drive the data electrode lines.
5. (CANCELLED)
6. (ORIGINAL) The field-sequential liquid crystal display panel of claim 1, wherein a capacitance of the storage capacitors is approximately 0.07 pF to 0.2 pF.
7. (ORIGINAL) The field-sequential liquid crystal display panel of claim 1, further

comprising a scan driver to drive the scan electrode lines.

8. (CURRENTLY AMENDED) A field-sequential liquid crystal display panel, comprising:

thin film transistors, each comprising a drain, a source, and a gate;
cell electrodes coupled to the drains or sources of the thin film transistors;
scan electrode lines coupled to the gates of the thin film transistors; and
storage capacitors to sustain a voltage applied to the cell electrodes;
wherein each of the storage capacitors ~~are each~~ is provided between one of the cell electrodes and below a scan electrode line coupled to the gate of the respective ~~one cell electrode through~~ one of the thin film transistors.

9. (CANCELLED)

10. (CANCELLED)

11. (ORIGINAL) The field-sequential liquid crystal display panel of claim 8, wherein a capacitance of the storage capacitors is approximately 0.07pF to 0.2pF.

12. (ORIGINAL) The field-sequential liquid crystal display panel of claim 8, wherein the voltage is sustained in the storage capacitors between an ending point of scanning each of the respective scan electrode lines and a starting point of a lighting time which is applied to ones of the cell electrodes.

13. (ORIGINAL) The field-sequential liquid crystal display panel of claim 8, further comprising a glass substrate, wherein the scan electrode lines are provided on the glass substrate.

14. (ORIGINAL) The field-sequential liquid crystal display panel of claim 13, further comprising:

data electrode lines to drive the cell electrodes; and
an insulating layer provided on the data electrode lines;
wherein the cell electrodes are formed on the insulating layer.

15. (ORIGINAL) The field-sequential liquid crystal display panel of claim 8, wherein the storage capacitors are formed by arranging the cell electrodes so that upper portions of the cell electrodes are disposed under the scan electrode lines.

16. (PREVIOUSLY PRESENTED) A field-sequential liquid crystal display panel having a plurality of cell regions, each cell region comprising:
a thin film transistor, comprising a drain, a source, and a gate;
a cell electrode coupled to the drain or source of the thin film transistor;
a scan electrode line coupled to the gate of the thin film transistor;
a data electrode line coupled to the source or drain of the thin film transistor; and
a storage capacitor provided between the cell electrode and a scan electrode line of an adjacent cell region.